

10 →

FIG. 1 is a block diagram of a system 10 for a prover and a verifier connected to a network 16 and multiple client/server terminals 18-1, 18-2, ..., 18-N.

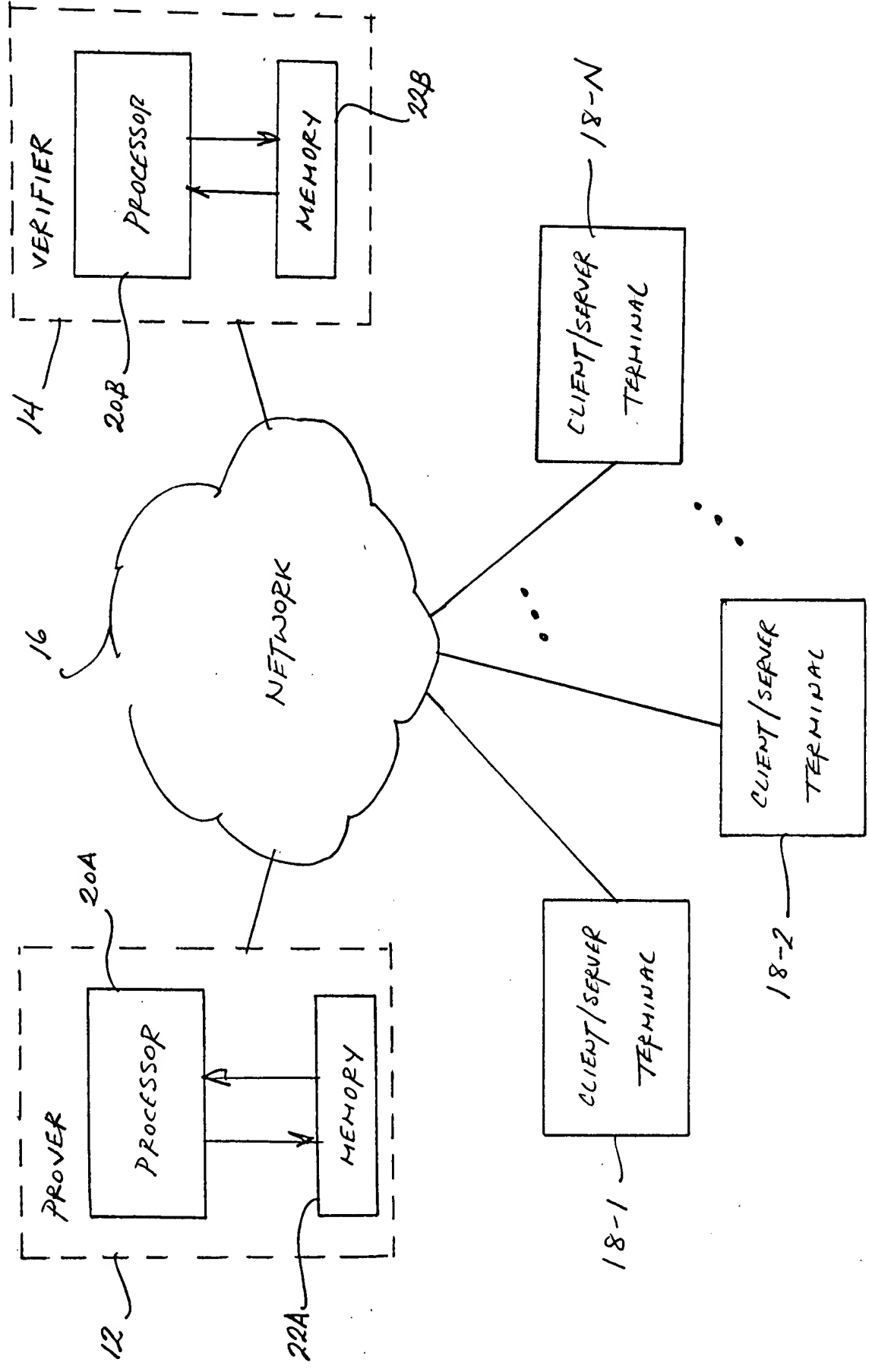


FIG. 1

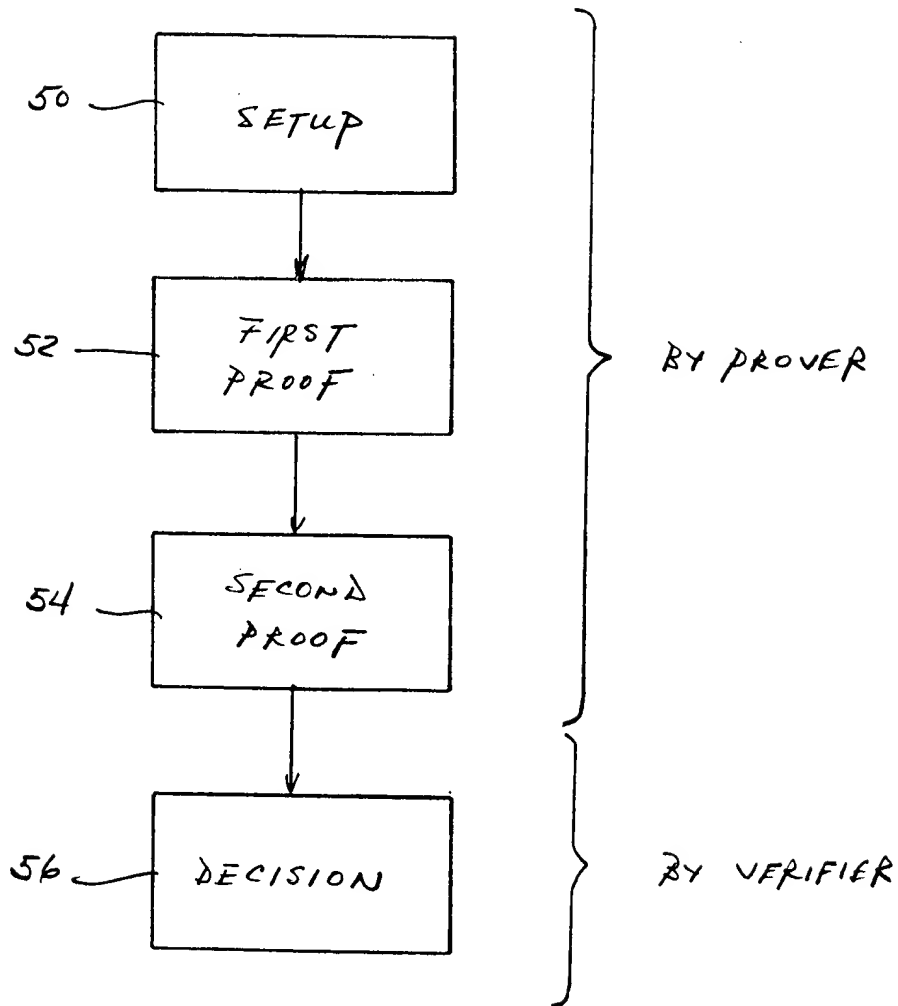


FIG. 2

FIG. 3

52
FIRST
PROOF

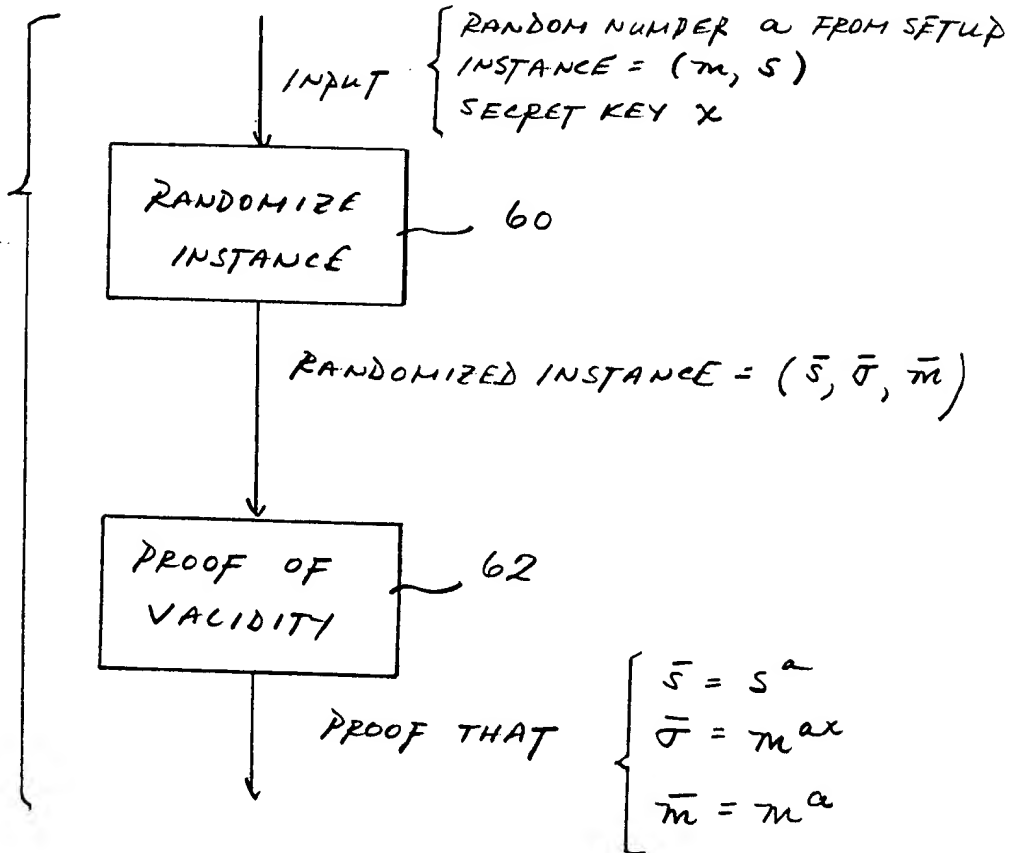
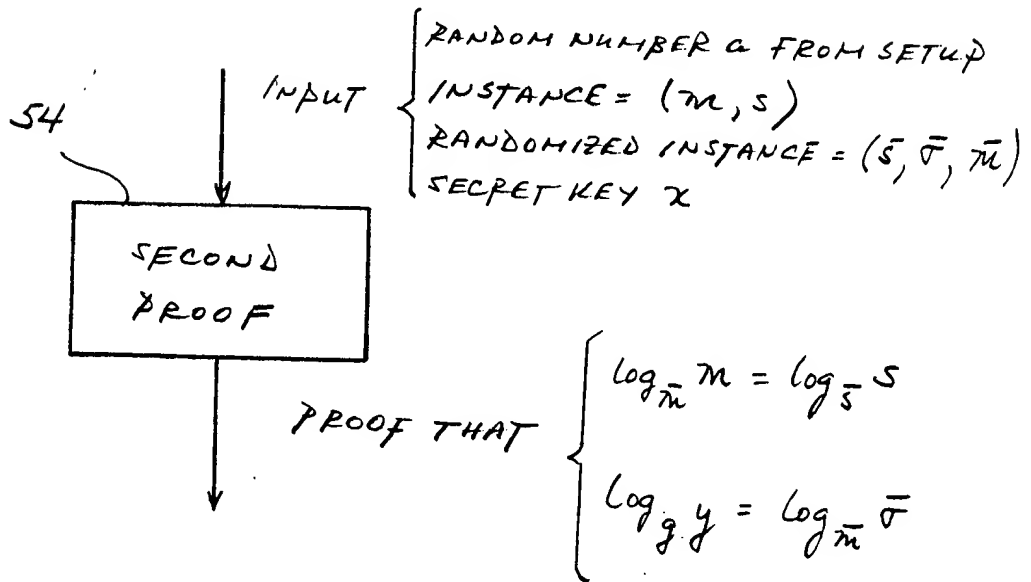


FIG. 4



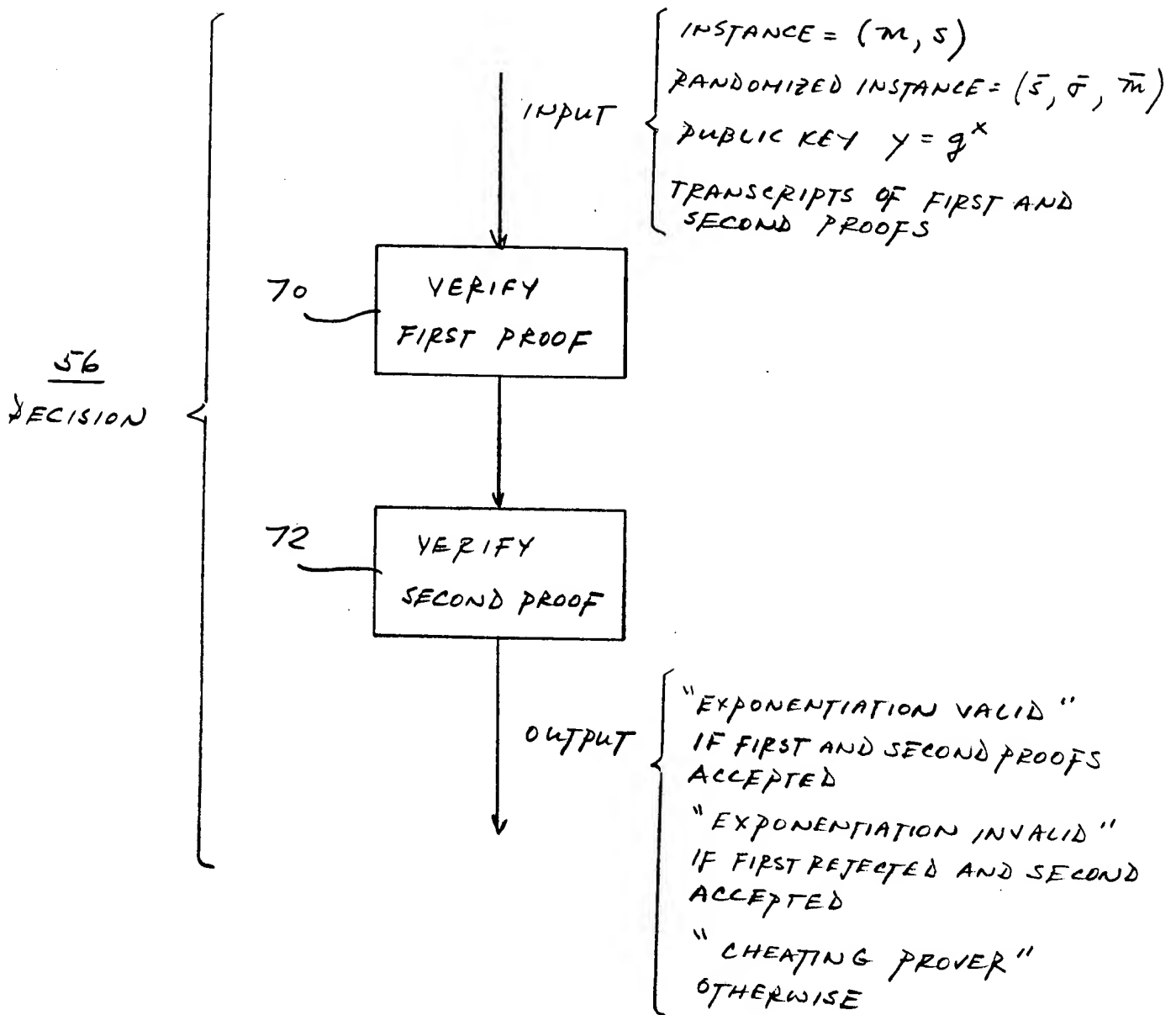


FIG. 5